

78K0R/Kx3 Microcontroller Sample Program Operation Manual (Input Pulse Interval Measurement (Timer Array Unit), ASM Source)

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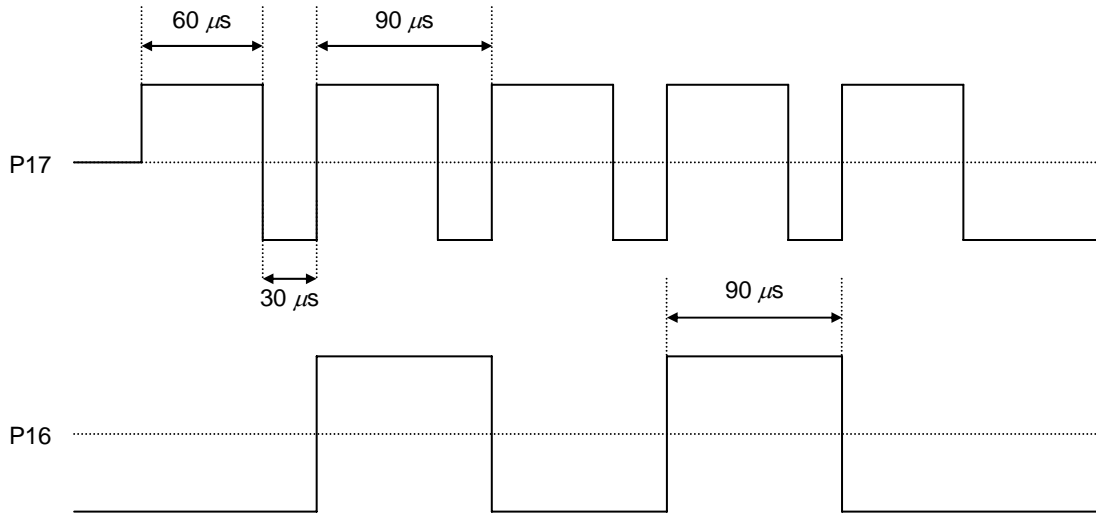
1. OVERVIEW

This manual explains the sample program functions of input pulse interval measurement for the 78K0R/Kx3 microcontroller.

In this sample program, the rising edge of P17 is detected by using timer channel 2.

When the rising edge of P17 is detected, INTTM02 is output. Each time INTTM02 is output, TMR02 is read and the high-level width of the pulse is measured (the measured pulse width is written to TDR02).

It is assumed that the pulse interval of the signal input to P17 is 90 μs (H/L = 60 μs /30 μs). P16 is used for checking the operation as it outputs a signal that reverses the high or low level when measuring the high-level width of P17 is completed.



2. RESOURCES USED

Resource	Description	Remark
Main clock specification	Internal high-speed oscillator used (8 MHz (TYP.))	Supplied to CPU and peripheral hardware
	High-speed system clock used (20 MHz)	Oscillated by initial processing
Subclock	XT1 (32.768 kHz)	Oscillated by initial processing
Related hardware	Peripheral enable register 0 (PER0)	Controls the input clock of the timer array unit.
	Timer clock select register 0 (TPS0)	Operation clock: CK01, 8 MHz (0.125 μ s)
	Timer mode register 02 (TMR02)	Operation clock: CK01, 8 MHz (0.125 μ s)
	Timer data register 02 (TDR02)	Stores the pulse width.
	Timer output mode register 0 (TOM0)	Channel 2 toggle operation mode
	Timer output level register 0 (TOL0)	Channel 2 positive logic output (active high)
	Timer output register 0 (TO0)	Channel 2 timer output value is "0".
	Timer output enable register 0 (TOE0)	Enables TO02 operation by counting operation.
	Timer channel start register 0 (TS0)	
	Timer channel stop register 0 (TTO)	
	Port mode register (PM1)	
Port register (P1)		
I/O	Input: P17 Output: TO02 (P16)	
Interrupt	Timer channel 2	
Others	Not used	

3. SOFTWARE CONFIGURATION

Files

File Name	Processing Outline
K0R_vct.asm	Vector processing
K0R_init.asm	Initialization processing
K0R_main.asm	Main processing
K0R_sfr_set.asm	Input pulse interval measurement

4. FUNCTION EXPLANATIONS

[File name]

K0R_main.asm

Function

Function Name	Processing Outline	Argument	Return Value
MMA_STRT	Main routine	None	None

Function explanations

Function name	MMA_STRT
Processing	Main routine
Argument	–
Return value	–
Description	<p>Sets P16 to the output mode.</p> <p>Executes initialization processing and then starts input pulse interval measurement.</p> <p>P16's status is reversed when the interrupt request flag of timer channel 2 is set to ON. Then the interrupt request flag is cleared.</p>
Remark	–

[File name]

K0R_sfr_set.asm

Functions

Function Name	Processing Outline	Argument	Return Value
STM_CINI	Initializes input pulse interval measurement.	None	None
STM_CSTT	Starts input pulse interval measurement operation.	None	None
STM_CSTP	Stops input pulse interval measurement operation.	None	None

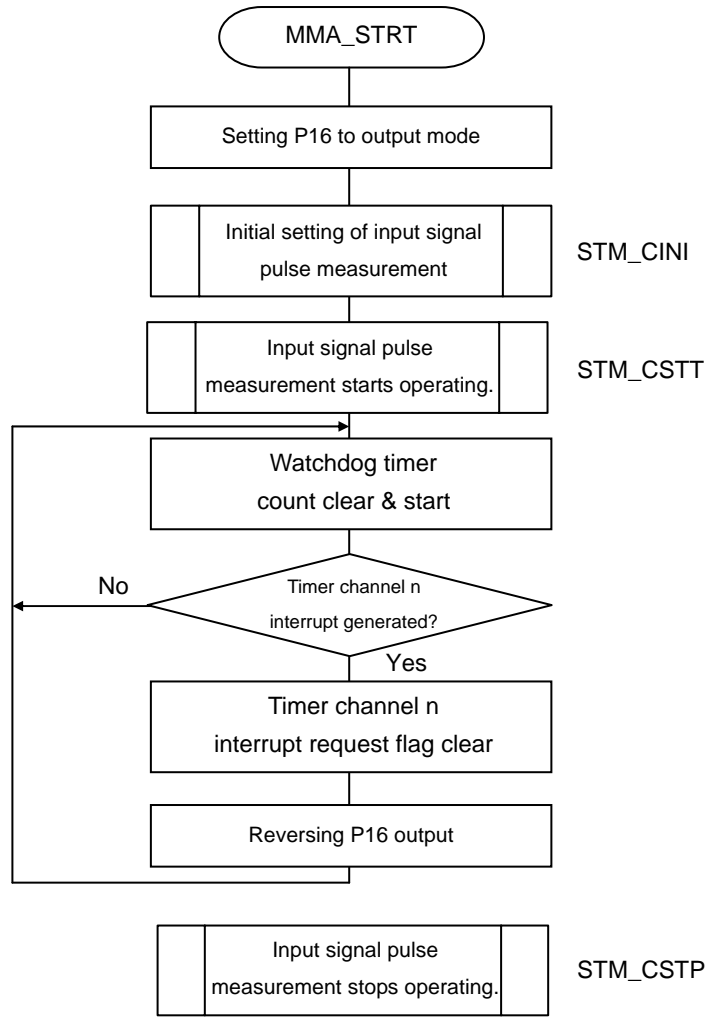
Function explanations

Function name	STM_CINI
Processing	Initializes input pulse interval measurement.
Argument	–
Return value	–
Description	<p>Initializes the timer array unit.</p> <ul style="list-style-type: none"> • Supplies a timer array unit input clock. <p>Initializes timer channel 2.</p> <ul style="list-style-type: none"> • Operation mode: Operation clock CK01, selection of the valid edge of the TI02 pin input, rising edge detection, capture mode • Output mode: Toggle operation mode
Remark	–

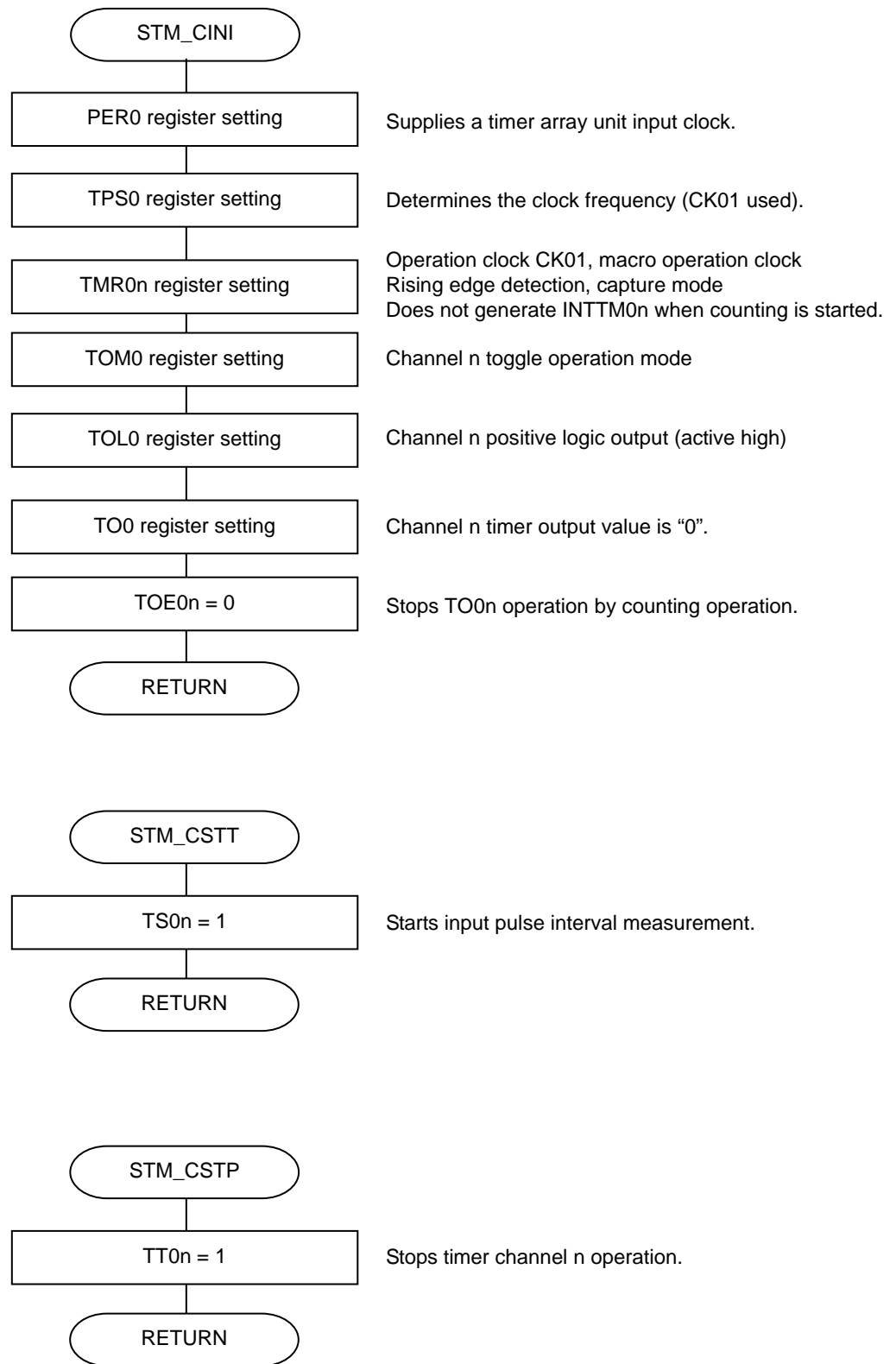
Function name	STM_CSTT
Processing	Starts input pulse interval measurement operation.
Argument	–
Return value	–
Description	Starts timer channel 2 operation.
Remark	–

Function name	STM_CSTP
Processing	Stops input pulse interval measurement operation.
Argument	–
Return value	–
Description	Stops timer channel 2 operation.
Remark	–

5. FLOWCHARTS



Remark n = 0 to 7 can be set.
 n = 2 for this sample program.



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 n = 2 for this sample program.